

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To be Assigned

Examiner: To be Assigned

In re PATENT APPLICATION of:

Applicant : Tadashi YAMAGUCHI)

Appln. No. : To Be Assigned)

Filed : Concurrently (As a Continuation of)
Application No. 10/057,909, filed)
January 29, 2002))

CLAIM FOR
PRIORITY

For : SEMICONDUCTOR DEVICE AND METHOD)
FOR MANUFACTURING THE SAME)

Attorney Ref. : OKI 284 C1)

Mail Stop: RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

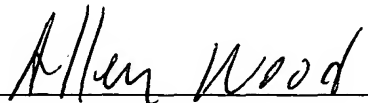
Sir:

It is respectfully requested that the above-identified continuation application be given the benefit under 35 U.S.C. §119 of the foreign filing date of Japanese Application Number 250482/2001, filed on August 21, 2001. A certified copy of the Japanese priority application was filed in the parent application (the parent application is number 10/057,909, filed January 29, 2002) on January 29, 2002.

It is respectfully requested that fulfillment of the requirements for claiming priority be acknowledged in the next communication from the Patent and Trademark Office.

Respectfully submitted,

July 18, 2003
Date


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AW:tlc